Form PTO-1449 (modified)	ATTY. DKT. NO. 5580-04402	SERIAL NO. 10/067,038
List of Patents and Publications		
For Applicant's Information	APPLICANT: Yeh	GROUP: 2183
Disclosure Statement		
(Use several sheets if necessary)	FILING DATE: February 4, 2002	

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A1	5,630,157	5/13/97	Dwyer, III			
	A2	6,195,744	2/27/01	Favor, et al.			
	A3	5,887,185	3/23/99	Lynch			
	A4	5,673,426	9/30/97	Shen, et al.			
	A5	5,933,618	8/3/99	Tran, et al.			
	A6	5,991,884	11/23/99	Lin, et al.			
	A7	6,282,663	8/28/01	Khazam			
	A8	6,219,796	4/17/01	Bartley			
	A9	5,666,506	9/9/97	Hesson, et al.			
	A10	5,655,096	8/5/97	Branigin			
	A11	5,625,837	4/29/97	Popescu, et al.			
	A12	5,546,593	8/13/96	Kimura, et al.			
	A13	5,497,499	3/5/96	Garg, et al.			
	A14	5,488,730	1/30/96	Brown, III, et al.			
	A15	5,487,156	1/23/96	Popescu, et al.			
	A16	5,790,827	8/4/98	Leung			
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	A18	Patterson and Hennessy, "Computer Architecture: A Quantitative Approach," Morgan Kaufmann Publishers, Inc., 1990, pages 291-299.
	A19	Patterson and Hennessy, "Computer Architecture: A Quantitative Approach," Second Edition, Morgan Kaufmann Publishers, Inc., 1990, 1996, pages 242-251.
	A20	SiByte, "Target Applications," http://sibyte.com/mercurian/applications.htm, January 15, 2001, 2 pages.
	A21	SiByte, "SiByte Technology," http://sibyte.com/mercurian/technology.htm, January 15, 2001, 3 pages.
	A22	SiByte, "The Mercurian Processor," http://sibyte.com/mercurian, January 15, 2001, 2 pages.
	A23	SiByte, "Fact Sheet," SB-1 CPU, October 2000, rev. 0.1, 1 page.
	A24	SiByte, "Fact Sheet," SB-1250, October 2000, rev. 0.2, 10 pages.

EXAMINER: _(signature)_   DATE CONSIDERED: 9-16-2004

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Information Disclosure Statement--PTO 1449 (modified)

Form PTO-1449 (modified)		ATTY. DKT. NO. 5580-04402		SERIAL NO. 10/067,038
List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)		APPLICANT: Yeh FILING DATE: February 4, 2002		GROUP: 2183

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

(initials)	A25	Stepanian, SiByte, "SiByte SB-1 MIPS64 CPU Core, Embedded Processor Forum 2000, June 13, 2000, 15 pages.
(initials)	A26	Jim Keller, "The Mercurian Processor: A High Performance, Power-Efficient CMP for Networking," October 10, 2000, 22 pages.
(initials)	A27	Tom R. Halfhill, "SiByte Reveals 64-Bit Core For NPUs; Independent MIPS64 Design Combines Low Power, High Performance," Microdesign Resources, June 2000, Microprocessor Report, 4 pages.
(initials)	A28	SiByte, Letter from Anu Sundaresan, May 18, 2000, 1 page.

EXAMINER: _(signature)_   DATE CONSIDERED: 9-16-2004

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U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
[initials]	B1	6,076,153	6/13/00	Grochowski, et al.	—		
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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